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*ybe*

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a REI of 08/311,021 09/22/1994 PAT 5,637,913  
 which is a DIV of 08/038,684 03/29/1993 PAT 5,378,656      *ybe*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 4-320098 11/30/1992

JAPAN 4-071116 03/27/1992

*ybe* see yr case 08/038 684

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 12/17/2001

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	21	27 13	8 4
Verified and Acknowledged	<i>antonelli</i> Examiner's Signature	<i>ybe</i> Initials			

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## TITLE

LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE USING THE SAME, AND METHOD OF  
 AND PROCESS FOR FABRICATING THE TWO

FILING FEE RECEIVED 1202	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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